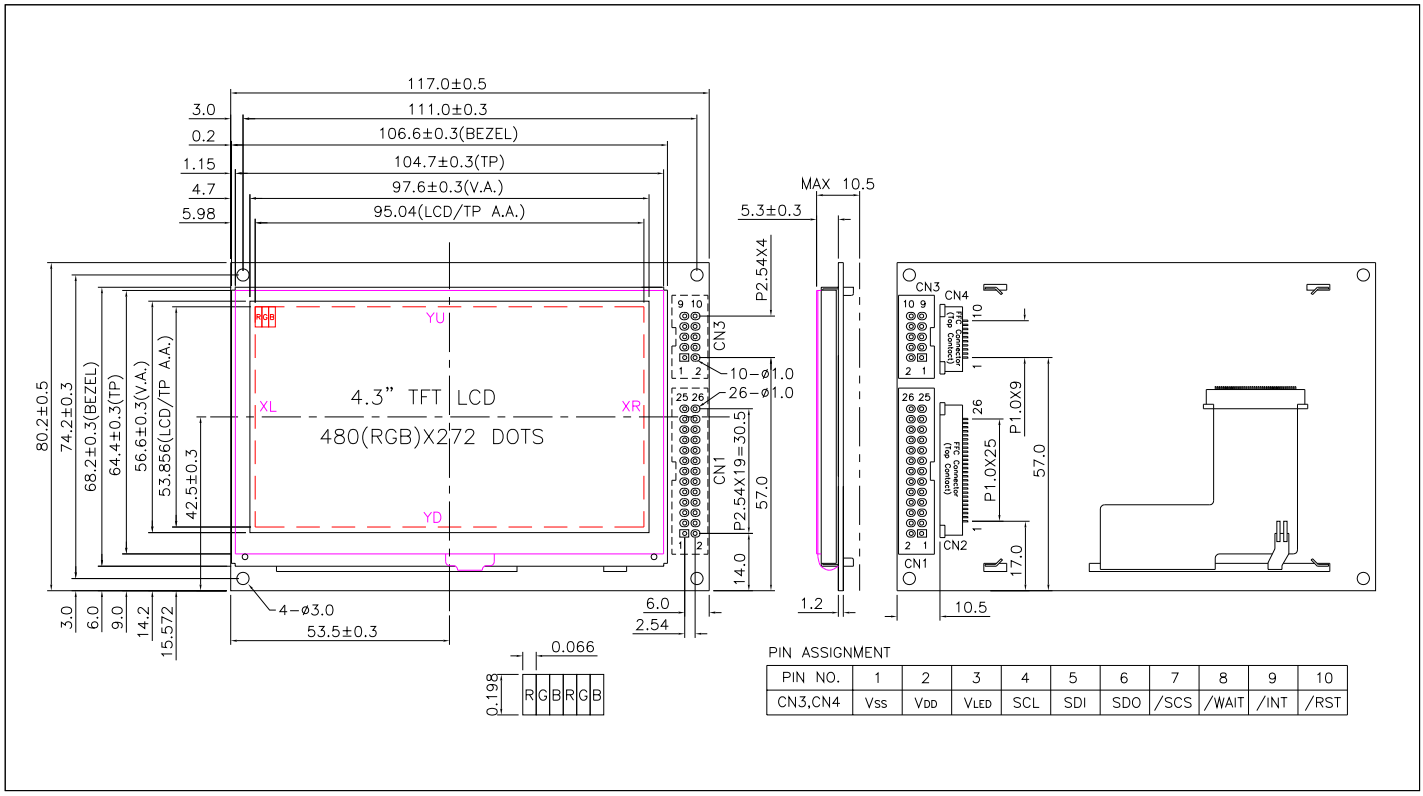
VS043C-50AT

4.3", 480(RGB) x 272 TFT + touch panel, 8/16-bit parallel or SPI, 3.3V to 5.5V, built in Chinese font



ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit
Supply Voltage(Logic)	V_{DD}- V_{SS}	-0.3	5.5	V
Supply Voltage(LED)	V_{LED}- V_{SS}	-0.3	6.0	V
Input Voltage	V_I	-0.3	V_{DD}+ 0.3	V
Operating Temp.	T_{opr}	-20	70	°C
Storage Temp.	T_{stg}	-30	80	°C

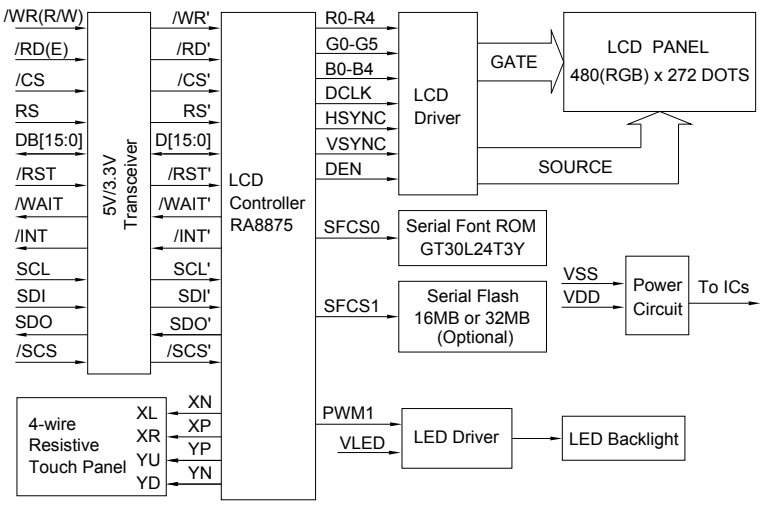
MECHANICAL DATA

Item	Nominal Dimensions	Unit
Module Size (W x H x T)	117.0 x 80.2 x 10.5	mm
Viewing Area (W x H)	97.6 x 56.6	mm
Active Area (W x H)	95.04 x 53.856	mm
Dot Pitch (W x H)	0.066 x 0.198	mm
Weight	Approx. 90	g

ELECTRICAL CHARACTERISTICS (V_{DD}=3.3V to 5.5V)

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input High Voltage	V_{IH}	--	2.0	--	V_{DD}	V
Input Low Voltage	V_{IL}	--	0	--	0.8	V
Output High Voltage	V_{OH}	I_{OH} = - 0.1mA	V_{DD}-0.2	--	V_{DD}	V
Output Low Voltage	V_{OL}	I_{OL} = 0.1mA	0	--	0.2	V
Supply Current (Logic)	I_{DD}	V_{DD}=3.3V to 5.5V	--	55	70	mA
Supply Current (LED)	I_{LED}	V_{LED} = 5.0V	--	80	100	mA

BLOCK DIAGRAM



PIN CONNECTIONS (CN1/CN2)

Pin	Symbol	Level	Function
1	V_{SS}	0V	GND
2	V_{DD}	3.3V to 5.5V	Power supply
3	V_{LED}	3.3V to 5.5V	Power supply for LED B/L driver
4	RS	H/L	Data or command selection H: Command L: Display data
5	/WR (R/W)	H/L	Write signal for 8080 MCU. R/W signal for 6800 MCU.
6	/RD (E)	H/L	Read signal for 8080 MCU. Enable signal for 6800 MCU.
7	/CS	L	Chip selection signal. Active "L".
8	/RST	L	Reset signal. Active "L".
9	/WAIT	L	Wait signal output. Active "L".
10	/INT	L	Interrupt signal output. Active "L".
11	DB0	H/L	Data bus for 8-bit data bus mode. Low order data bus for 16-bit data bus mode.
12	DB1	H/L	
13	DB2	H/L	
14	DB3	H/L	
15	DB4	H/L	
16	DB5	H/L	
17	DB6	H/L	
18	DB7	H/L	
19-26	DB8 to DB15	H/L	High order data bus for 16-bit data bus mode. Keep DB8 to DB15 open when 8-bit data bus mode is used.