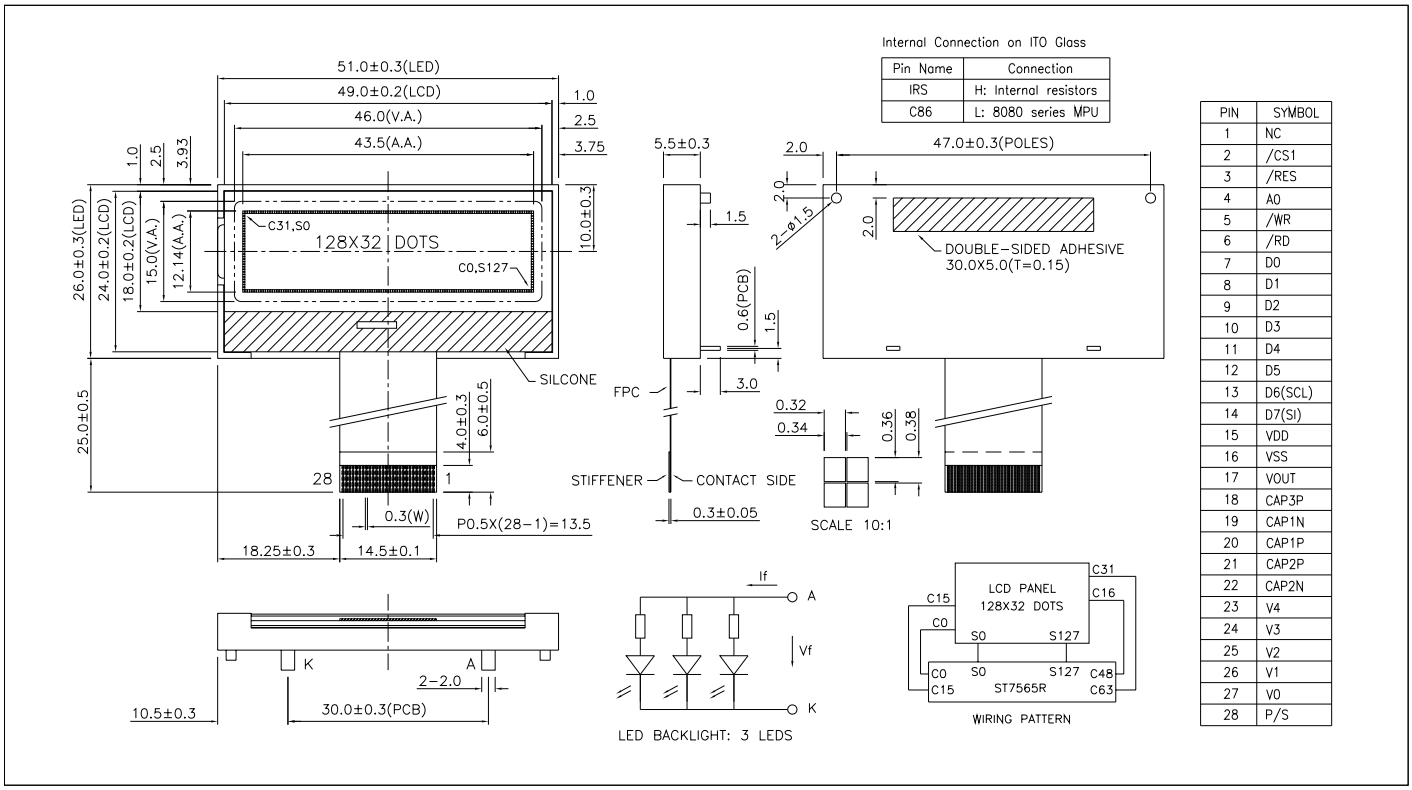
VS128324-DW

128 x 32 dots + white led backlight, 8-bit parallel or SPI, 3.3V



ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit
Supply Voltage(Logic)	V_{DD} - V_{SS}	-0.3	3.6	V
Supply Voltage(LCD)	V_{OUT} - V_{SS}	-0.3	13.5	V
Input Voltage	V_I	-0.3	V_{DD}+ 0.3	V
Operating Temp.	T_{opr}	-20	70	°C
Storage Temp.	T_{stg}	-30	80	°C

MECHANICAL DATA

Item	Nominal Dimensions	Unit
Module Size (W x H x T)	51.0 x 26.0 x 8.5	mm
Viewing Area (W x H)	46.0 x 15.0	mm
Dot Pitch (W x H)	0.34 x 0.38	mm
Dot Size (W x H)	0.32 x 0.36	mm
Weight	Approx. TBD	g

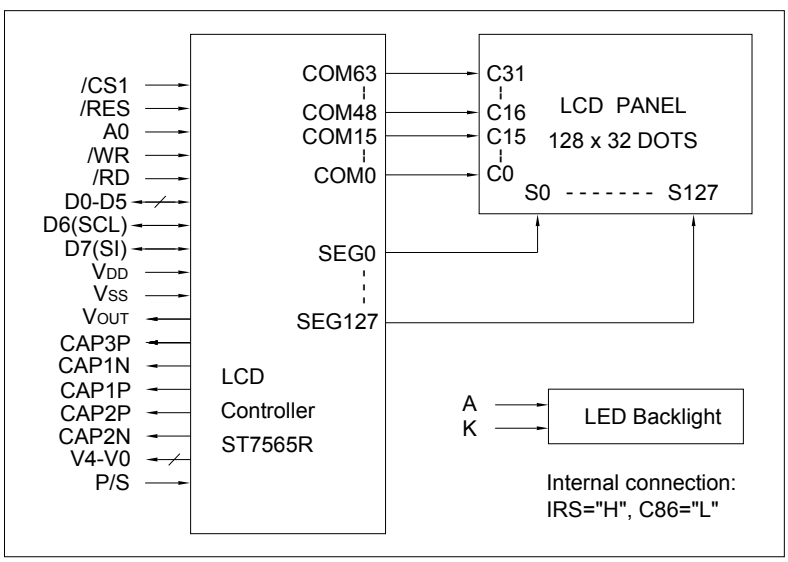
ELECTRICAL CHARACTERISTICS (V_{DD}=2.7V to 3.3V)

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input High Voltage	V_{IH}	--	0.8V_{DD}	--	V_{DD}	V
Input Low Voltage	V_{IL}	--	0	--	0.2V_{DD}	V
Output High Voltage	V_{OH}	I_{OH} = - 0.1mA	0.8V_{DD}	--	V_{DD}	V
Output Low Voltage	V_{OL}	I_{OL} = 0.1mA	0	--	0.2V_{DD}	V
Supply Current	I_{DD}	V_{DD} = 3.3V	--	120	200	uA
LCD Driving Voltage	V_0 - V_{SS}	Ta=25°C	--	7.6	--	V

PIN CONNECTIONS

Pin	Symbol	Level	Function
1	NC	--	No connection
2	/CS1	L	Chip selection signal. Active "L".
3	/RES	L	Reset signal. Active "L".
4	A0	H/L	H:Display data L:Instruction code
5	/WR	L	Write signal. Active "L".
6	/RD	L	Read signal. Active "L".
7-12	D0-D5	H/L	P/S=H: D0 to D7 are 8-bit data bus.
13	D6(SCL)	H/L	P/S=L: D6 is serial clock input;
14	D7(SI)	H/L	D7 is serial data input.
15	V_{DD}	2.7 to 3.3V	Power supply for logic and booster
16	V_{SS}	0V	GND
17	V_{OUT}	--	DC/DC voltage converter output
18	CAP3P	--	Capacitor 3 positive connection
19	CAP1N	--	Capacitor 1 negative connection
20	CAP1P	--	Capacitor 1 positive connection
21	CAP2P	--	Capacitor 2 positive connection
22	CAP2N	--	Capacitor 2 negative connection
23-27	V4-V0	--	Power supply for LCD drive
28	P/S	H/L	H: Parallel mode L: Serial mode

BLOCK DIAGRAM



LED BACKLIGHT SPECIFICATIONS (Ta=25°C)

Item	Symbol	Typ.	Max.	Unit
Forward Voltage	V_f	2.8	3.0	V
Forward Current	I_f	20	--	mA
LED Color	White			